REMARKS/ARGUMENTS

1. Claim Amendments

Claims 40, 44, and 46 – 47 have been amended to correct for grammatical errors. No new matter is added.

5   ### 2. Claim Rejections – 35 U.S.C. 112

Claims 1 – 13 and 51 were rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. Additionally, Claim 51 was rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

10   <u>Response</u>

<u>Claim 1</u>

Claim 1 states that each cell of the divider is returned to a predetermined respective state in response to the reset signal. Support for this claim is taken from Fig.9, showing that the outputs of the upper flip-flops of the cell are set to logic 1, where the outputs in

15   this case are the Q outputs. The Examiner states that specification [Para 66] discloses that the first flip-flop outputs a logic 0 in response to the reset signal. The applicant would like to point out that the output of logic 0 is from the cell, not from the flip-flop, and that the flip-flops are contained in the cell (Fig.9: flip-flop numerals = 2, 4). As the first and the second flip-flop are coupled together by means of the Q bar output, however, the Q bar

20   outputs can also be regarded as the outputs of the flip-flops. In this case, both Q bar outputs are set to a logic 0 in response to the reset signal, which is also the claimed 'predetermined respective state'. Therefore, regardless of whether the Q outputs or the Q bar outputs are regarded as the flip-flop outputs, the reset signal will always return each

cell of the divider to a predetermined respective state. Furthermore, specification [Para 44] states that: "the flip-flops 2 and 4 are both at reset state such that the cell 10 does not perform a dividing operation" and "After reset is disabled, e.g., the reset signal returns to logic 0 level, the cell 10 renews its dividing operation". It can be seen that when the

5 flip-flops are in the reset state the cell itself will be in a reset state, and as the cell cannot perform the dividing operation while in the reset state, said reset state is a predetermined state independent of the divisor signal state. Therefore, applicant asserts that the limitations in Claim 1 are fully supported by the specification, and the claim rejections under 35 U.S.C. 112, first paragraph, have been overcome accordingly. Claim 1 should

10 therefore be found allowable. Reconsideration of Claim 1 is respectfully requested.

Claims 2 – 13

As the applicant asserts that the limitations of Claim 1 are fully supported by the specification, and as claims 2 – 13 are dependent on Claim 1, claims 2 – 13 should also be found allowable.

15 Claim 51

Claim 51 has been cancelled.

3. Allowable Subject Matter

Claims 14 – 21, 23 – 42, and 44 – 50 were allowed.

Response

20 The applicant is grateful to the Examiner for the allowance.

Claims 10 – 12 would be allowable if rewritten in independent form with the limitations of the base claim and all intervening claims

Response

As argued under the response to Claim 1, the applicant believes that claims 10 – 12 have been placed in a position for allowance.

5   Applicant respectfully requests that a timely Notice of Allowance be issued in this case.

Sincerely yours,

[signature: Winston Hsu]   Date: 06.07.2007

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